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CLAIMS

1. A method for visualizing circuit operation,
comprising:
 2. a. obtaining device activity based on one or
more of measured or simulated activity;
 3. b. expressing the device activity in a
representation; and
 4. c. representing the expressed activity in a
visual form.
5. 2. A method according to claim 1, wherein said
representation includes sequence, connectivity and causal
relationship information.
6. 3. A method according to claim 1, wherein said
representing step includes the step of visualizing the
expressed activity in an IC CAD viewer.
7. 4. A method according to claim 1, wherein said
representing step includes the step of visualizing the
device activity representation as a simulation of optical
emissions that occur as a result of the device activity.
8. 5. A method according to claim 1, wherein the obtaining
step includes the steps of:
 1. applying device activity traces as inputs to
the circuit; and
 2. measuring sequences of logical states at
designated elements.

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3* 6. A method according to claim 5, wherein the expressing step includes the step of expressing the measured sequences in a sequence graph format.

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3* 7. A method according to claim 1, wherein said obtaining step includes the step of obtaining an activity trace based on one or more of measured or simulated activity.

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2* 8. A method according to claim 1, wherein the visual form is a slow motion animation.

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2* 9. A method according to claim 8, wherein the slow motion animation is a video visualization.

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2* 10. A method according to claim 1, wherein the visual form is an animated schematic.

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4* 11. A method according to claim 10, wherein in the animated schematic, the devices or collection of devices appear highlighted, or change color, shape or otherwise visualize the occurrence of switching.

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4* 12. A method according to claim 1, wherein audio representation of circuit activity augments the visualization by the occurrence of sound in conjunction with the visual indication of circuit activity.

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3* 13. A method according to claim 12, wherein the audio frequency or other audio character is related to the timing relationships of the switching events.

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2 14. A method according to claim 13, wherein the timing
3 relationships of the switching events include delay from
4 prior switching event, or device transition speed, or
input to output delay.

1 15. A method according to claim 1, wherein switching
2 behavior is mapped to a mathematical graphical
3 representation which is related to a netlist.

1 16. A method according to claim 4, further comprising the
2 step of modeling the emissions as a hot electron
3 photoluminescence model.

1 17. A method according to claim 4, further comprising the
2 step of assigning the emission based on a two-state
3 (optically active or not) model according to whether the
4 device is switching or not.

1 18. A method according to claim 17, wherein the method of
2 determining the switching state of a device is by
3 thresholding the current.

1 19. A method according to claim 17, further comprising
2 the step of assigning the switching state by checking for
3 logical state (0 or 1) transitions at nets corresponding
4 to the terminals of a device to detect if the device
5 switches in response to the input level(s) to the device.

1 20. A method according to claim 4, wherein an areal (x-y)
2 view of the simulation is produced from the simulation
3 emission.

- 1 21. A method according to claim 1, further comprising the
2 step of designating regions of a device as an array of
3 "pixels" overlaid to the device.
- 1 22. A method according to claim 20, wherein the areal
2 distribution model is a Gaussian distribution from point
3 sources from designated areas of the device.
- 1 23. A method according to claim 22, wherein the
2 illumination intensity at each pixel results from a Monte
3 Carlo simulation of events.
- 1 24. A method according to claim 1, wherein the visual
2 form is a current flow animation.
- 1 25. A method according to claim 1, wherein the visual
2 form is a local power dissipation animation.
- 1 26. A method according to claim 1, wherein the visual
2 form is a verification trace animation.
- 1 27. A method according to claim 1, wherein the simulated
2 activity is a circuit electrical simulation and is
3 conducted for manufacturing test and subsequently
4 animated.
- 1 28. A method according to claim 1, wherein the visual
2 form is a sequence graph depicting the causal order of
3 waveform transition events.

- 1* 29. A method according to claim 27, wherein the
2 electrical simulation is conducted for manufacturing test
3 and subsequently animated for optical emission.
- 1* 30. A method according to claim 1, wherein optical
2 emission measurement data is compared to optical emission
3 simulation data and the regions (in x,y,t) of agreement
4 and/or disagreement between the two are identified.
- 1* 31. A method according to claim 1, wherein logical state
2 data gathered from optical emission measurement is
3 compared to logical state data from simulation and the
4 areas (in x,y,t) of agreement and/or disagreement between
5 the two are identified.
- 1* 32. A method according to claim 1, wherein the expressing
2 step includes the step of expressing the device activity
3 in a sequence graph format.
- 1* 33. A method according to claim 32, wherein the sequence
2 graph is derived from a netlist or schematic, and
3 comprises a record of the events that occurred within the
4 network as a result of the system input.
- 1* 34. A method according to claim 1, wherein the obtaining
2 step includes the step of obtaining optical emissions
3 from the circuit as a result of stimuli input to the
4 circuit.
- 1* 35. A method according to claim 34, wherein the optical
2 emissions are generated by switching activity caused by
3 the input stimuli.

1 36. A method according to Claim 1, wherein:
2 the obtaining step includes the steps of
3 i) using an instruction trace to obtain a first
4 representation of device activity, and
5 ii) using a testvector sequence to obtain a
6 second representation of device activity; and
7 further including the step of comparing the
8 first and second representations to determine how well
9 the testvector recreates the activity generated by the
10 instruction trace.

1 37. A method according to Claim 1, wherein:
2 the obtaining step includes the step of using a
3 testvector sequence to cause device activity; and
4 further including the step of analyzing said
5 device activity to verify or debug the testvector
6 sequence.

1 38. A method according to claim 1, wherein the circuit is
2 an asynchronous circuit.

1 39. A system for visualizing circuit behavior,
2 comprising:
3 a. means for simulating circuit activity;
4 b. means for expressing the circuit activity as
5 a device activity representation, and
6 c. means for visualizing the device activity
7 representation as a simulation of optical emissions that
8 may occur as a result of device activity.

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40. A system according to claim 36, wherein said device activity representation includes sequence, connectivity and causal relationship information.

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41. A system according to claim 36, wherein said means for visualizing includes an IC CAD viewer for visualizing the expressed activity.

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42. A system according to claim 36, wherein said means for visualizing includes means for visualizing the device activity representation as a simulation of optical emissions that occur as a result of the device activity.

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43. A system according to claim 36, wherein the means for simulating circuit activity includes:

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means for applying device activity traces as inputs to the circuit; and

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means for measuring sequences of logical states at designated elements.